Fault-tderant optical quantum computation with duster states

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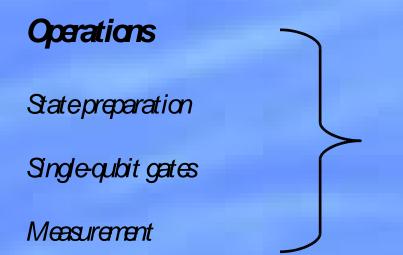
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An optical quantum computer?

| 0, ´ horizontally polarized singlephoton | 1, ´ vertically polarized singlephoton Very long decoherence times



Ourrent technology adequate for basic experiments

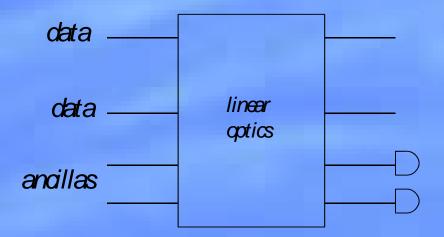
An optical quantum computer?

Can wedban entanglinggate?

 $OPHASE | x_i | y_i = (-1)^{y_i} | x_i | y_i$

Impossible with linear optics alone

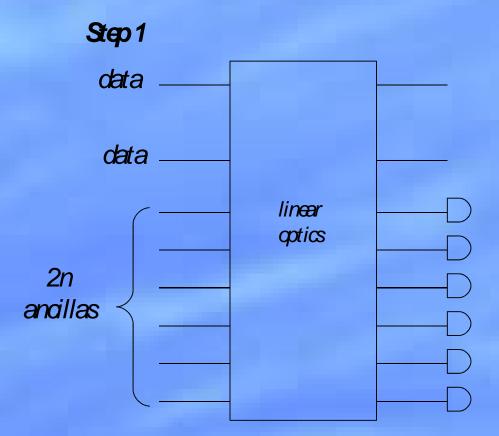
Knill-Laflamme-Milburn (Nature, 2001) showed how to do this in a non-deterministic but heralded fashion.



Success A single photon is measured at each port. Occurs with probability 1/4.

Failure Data masured in the computational basis

KLM increase the probability of successusing two steps



Success A single photon is measured at each port. Occurs with probability $(n/(n+1))^{p}$.

Failure Data measured in the computational basis

Making n large increases the success probability, but makes doing the gate harder.

Sequential performance of the gate

Interact and illas with data qubit 1.

Measure half the ancillas! success probability n/(n+1).

Interact and llas with data qubit 2.

Measure half the ancillas! success probability n/(n+1).

Step 2 for increasing the probability of success Probability of success can be boosted doser to 1 using quantum error-correction.

Disadvantage Probability of success dose to 1 requires 10⁴-10⁵ optical dements to do a single entangling gate

Istherea better way?

We'll use the n = 2 gate, which succeeds with probability $(2/3)^2$.

Higher values of n turn out not to be necessary.

No error-correction is required.

Morelike 10° optical dements for a OPHASE gate

The key is to combine then = 2 KLM gate with the **one way quantum computer** or **duster state** model of quantum computation (Raussendorf and Briegel, PRL 2001).

Overview of duster-state computation

Threesteps

1. Preparea many-qubit state, the duster state

- 2. Performa sequence of adaptive single qubit measurements on some subset of the duster qubits
- 3. The remaining qubits are the output of the computation.

These three steps can be used to simulate an arbitrary quantum circuit.

Defining the duster state

Each noder epresents

Wedefine the duster state as the result of the following two-stage

Wedefine the duster state as the result of the following two-stage preparation procedure

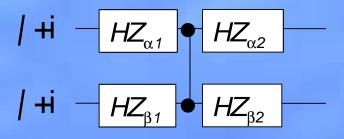
1. Prepare each qubit in the state | +i ´ | 0 + | 1i

2. Apply a OPHASE gate between each pair of connected qubits

It doesn't matter in which order the CPHASE gates are applied.

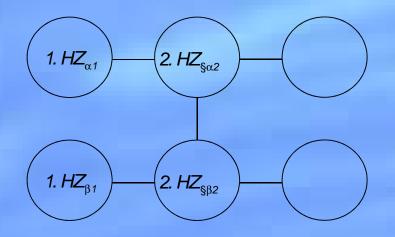
Recipe to simulate a quantum circuit

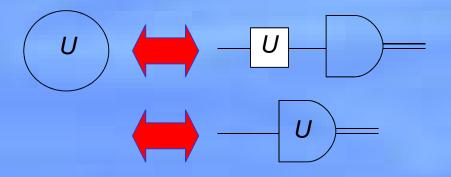
Grait tobe simulated



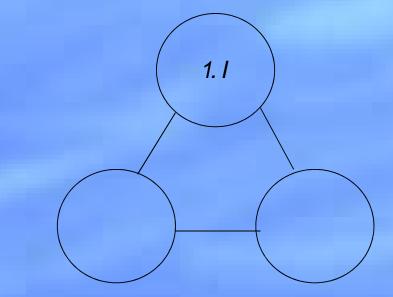
The gates HZ_{α} and OPHASE are universal.

Theduster-statesimulation





What happens if we measure a duster qubit in the computational basis?



Measuring a duster qubit in the computational basis simply removes it from the duster.

Combining duster-state computation with the KLM (2/3)² gate

Suppose we've built up part of a duster...

And now weatterpt to add a qubit.

Success With probability 2/3 weadd a qubit to the duster.

Failure With probability 1/3 welose a qubit from the duster.

On average, we add 1/3 of a qubit to the duster, per KLM gate.

Of course, it's not enough just to build up a linear duster, we need a planar duster.

Buildinggeneral dustersusing the KLM (2/3)² gate

Can build up a general duster using similar random walk ideas

Result: On average, we add 1/9 th of a qubit to the duster per KLM gate performed.

Summary

Basic KLM gate with success probability (2/3)² can be used to quickly build up dusters



Optical quantum computation (Nielsen, PRL, 2004).

Why this works

- 1. Failure mode of KLM gate is a computational basis measurement. Coincidentally, such a measurement simply deletes a duster qubit.
- 2. Because the duster is a **fixed** state, it's okay to lose a qubit, provided we can rebuild. In particular, losing quantum information is not a problem!

What about noise?

A proposal for quantum computation should be able to tolerate a reasonable level of physical noise.

> in abstract circuit models, the techniques of fault-tolerant quantum computation enable a threshold for quantum computation

For most proposals (eg superconductors, KLM, ion trap, .), a physical threshold value follows from straightforward modifications of theoretical threshold constructions

With dusters, fault-tolerance is less obvious

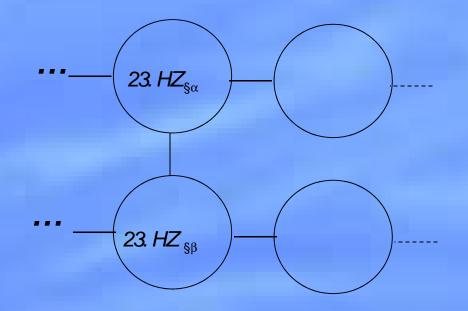


In principle resolution found by Nielsen and Dawson (quant-ph, 2004). C.f. Raussendorf (thesis, 2003)

Twoprediens in proving a threshold

1. If we prepare too much of the duster at once, some qubits will decohere

Possiblesclution



Only add extra qubits slowly into the duster: 'just-in-time' preparation.

2. If webuild the duster up just-in-time, won't the stochastic nature of the KLM gate make erasing the duster a possibility?

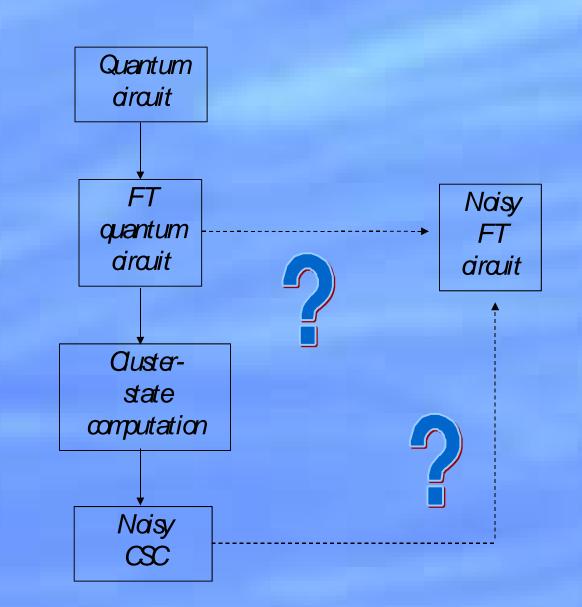
Yes, but this can be dealt with by building error-correction into the duster.

Basicidea

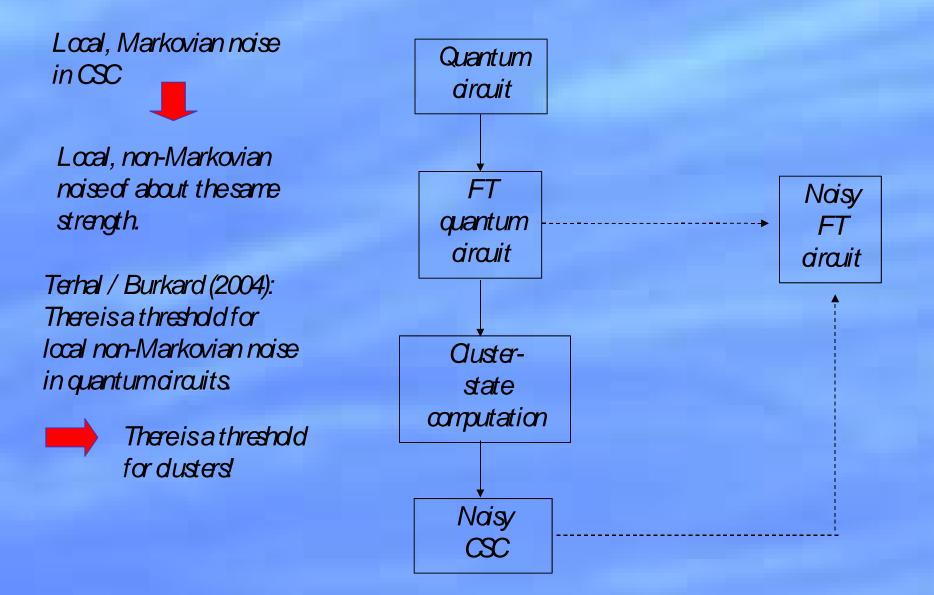
Q: I sit possible to map noise in the CSC back to equivalent noise in the FT circuit?

Q: Is "physically reasonable" noise in the CSC mapped back to physically reasonable noise in the FT circuit?

Yes Such a mapping can be constructed. (I nvol ved.)



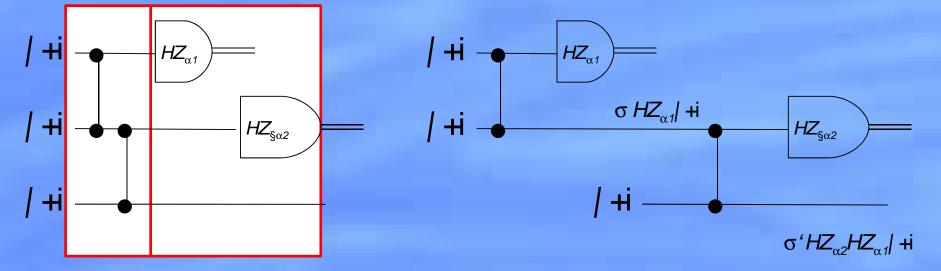
What properties does the noise mapping have?





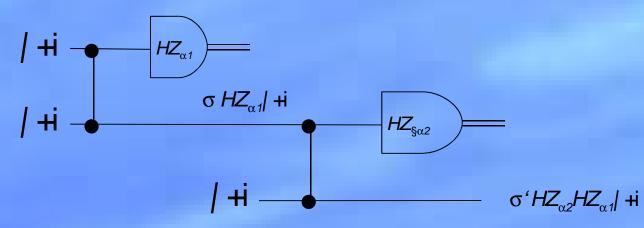
Standard implementation

Buffered implementation

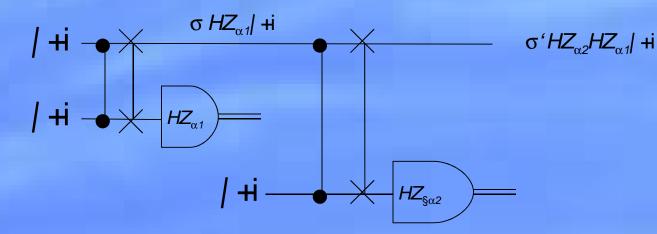


Twopaints of view: Well be interested in both ideal arouit, and the real implementation of the arouit, with noisy dements

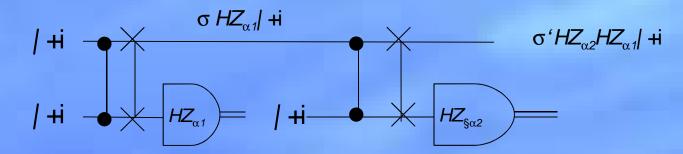
(Noisy) buffered duster state computation



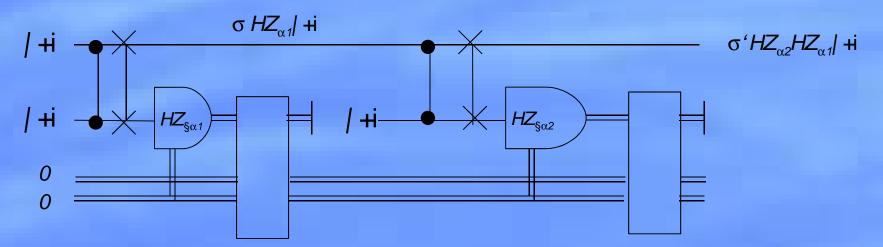
Equivalent to:



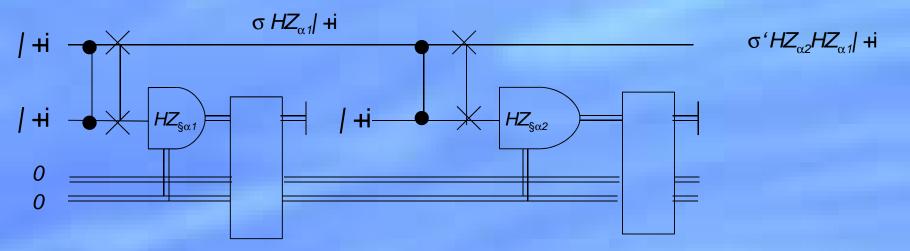
In a more compact form



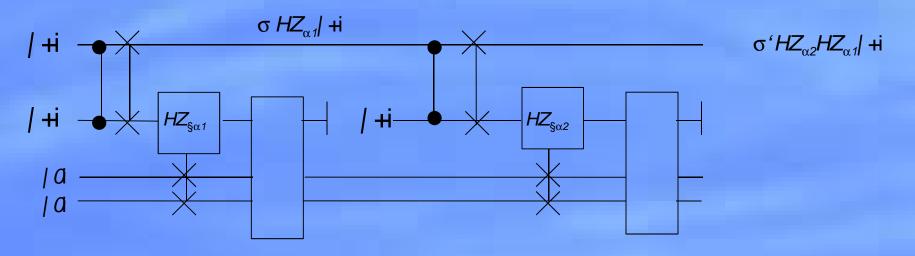
Insert dassical drauitry explicitly.



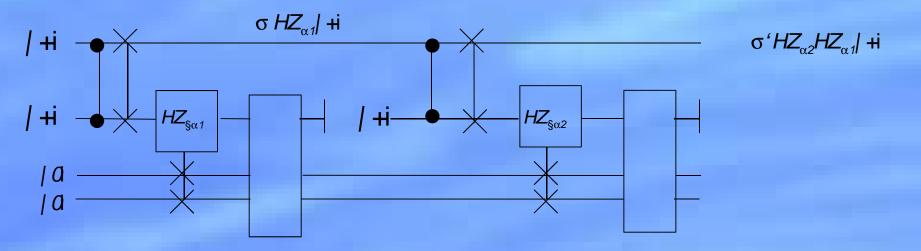
With dassical drauitry



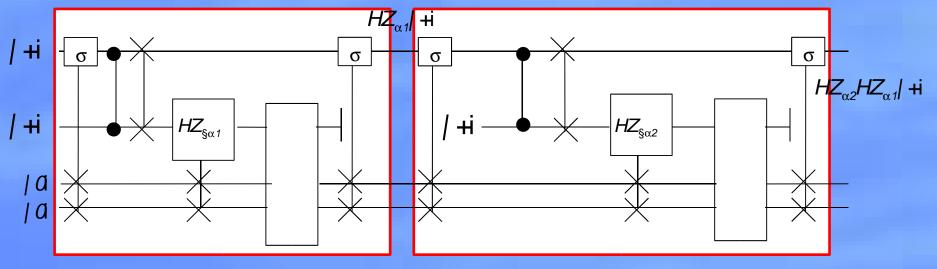
Changedassical to quantum



After changing dassical to quantum

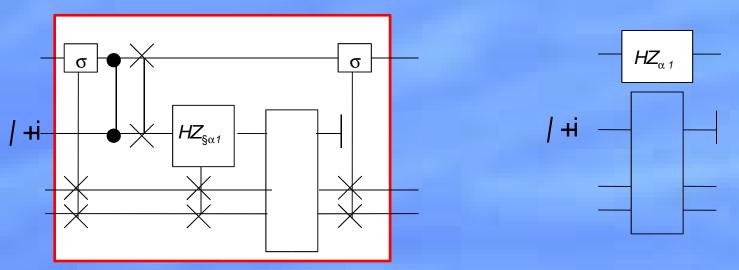


Inserting the identity:



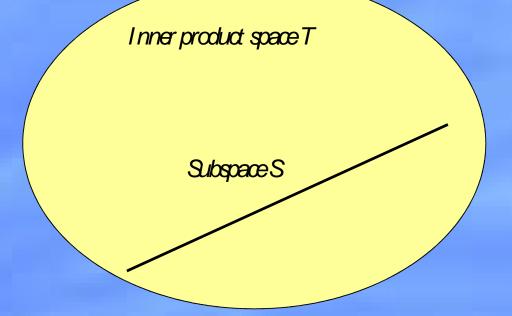
The duster state computation is made up of repeating blocks of the form

When done perfectly this has the effect:



Intuitively, when some of the dements on the left are done imperfectly, we will get a noisy version of the gate on the right.

The rigorous connection to the noise model of Terhal and Burkard may be made using the **unitary extension theorem**



Let U and V beunitaries on T.

U and V may have quite different actions on T, but be quite similar on S

E.g., |U-V| may be large, while $|U_s - V_s|$ is small.

The unitary extension theorem guarantees the existence of a unitary W such that

(c) $W_{\rm S} = V_{\rm S}$

(d)
$$|U - W| \cdot 2 |U_{s} - V_{s}| = 2 |U_{s} - V_{s}|$$

Can extend the noise mapping to multiple-qubit duster state computation using similar ideas

The extension to optical duster-state computation involves similar ideas, but also some additional ideas to cope with the cocasional failure of the OPHASE.

Candusian

Wearenow doing numerical investigations of the threshold, basing our approach on Steane's threshold construction.

Goal is to see how (optical) duster-state thresholds compare with standard constructions

Best possible threshold?

Dosimpleproof-of-principle experiments Develop better sources and detectors